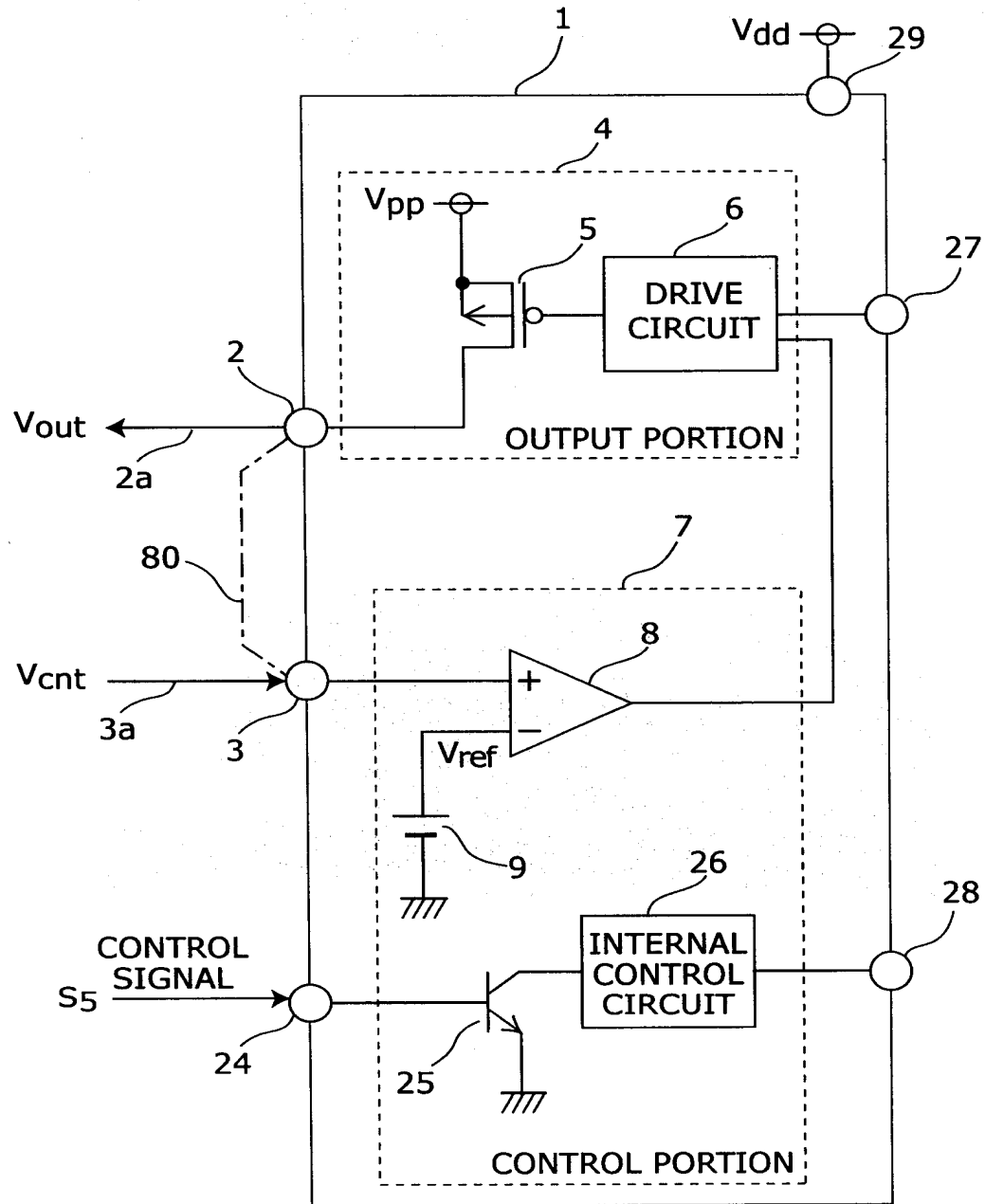


FIG. 1



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FIG. 2

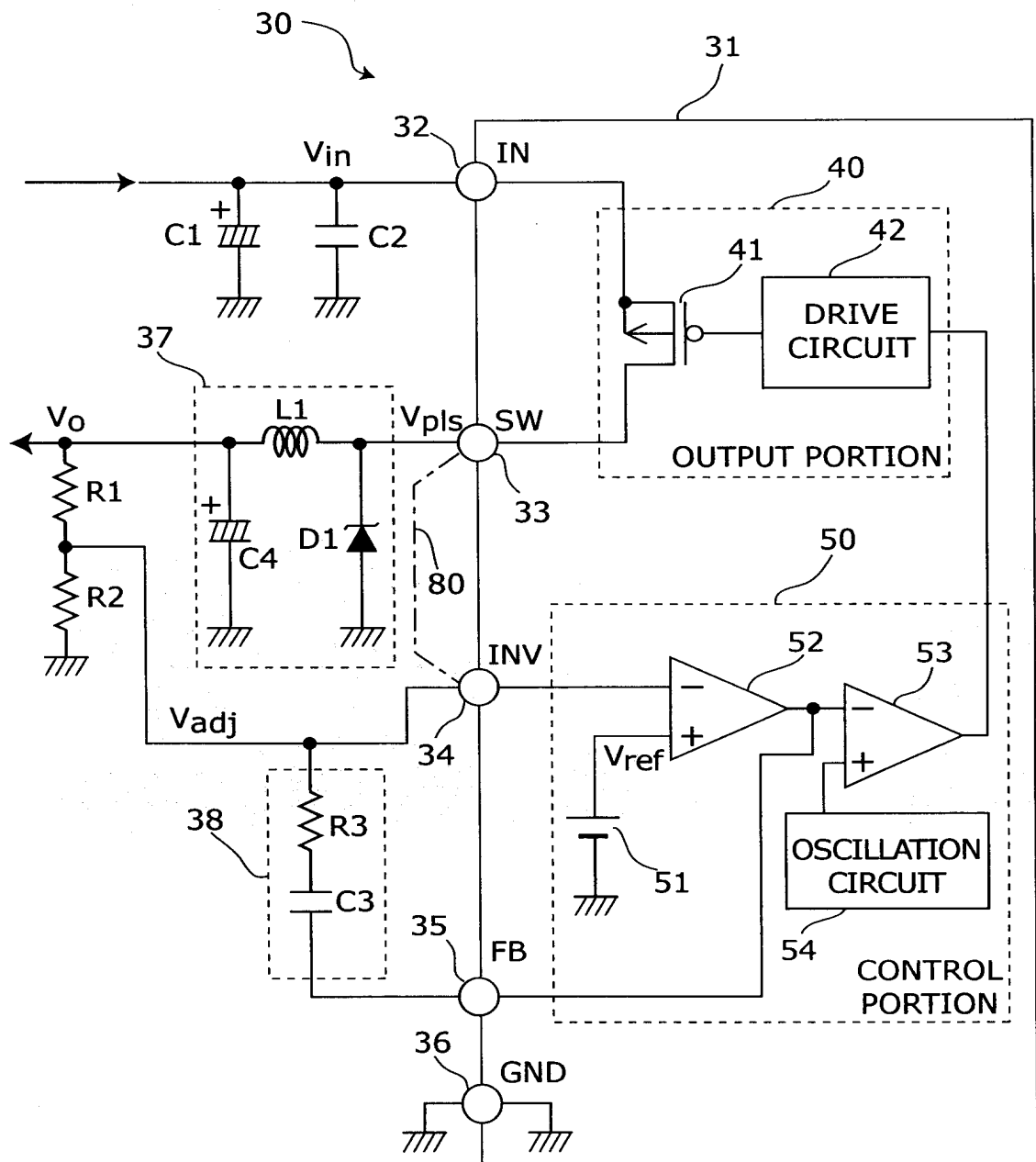


FIG. 3

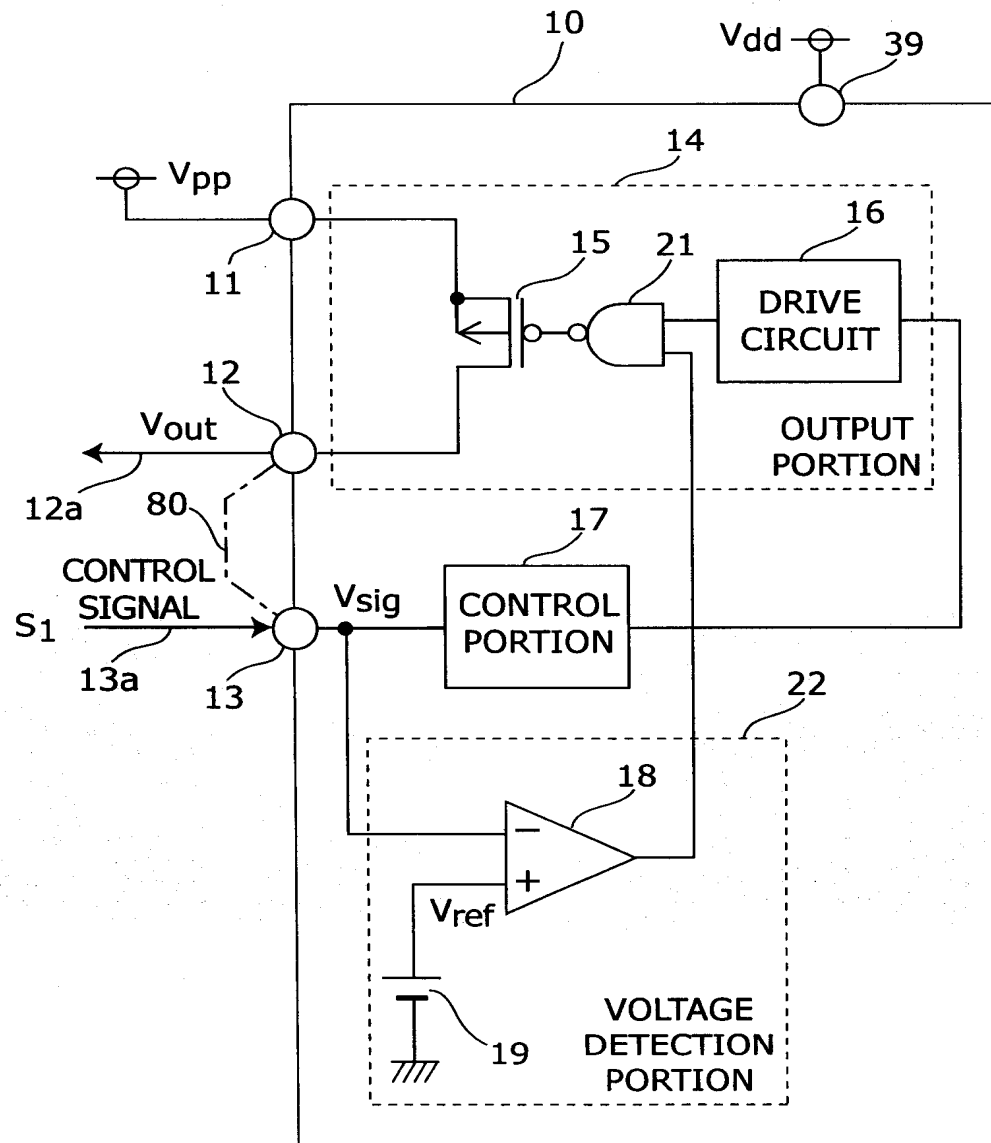
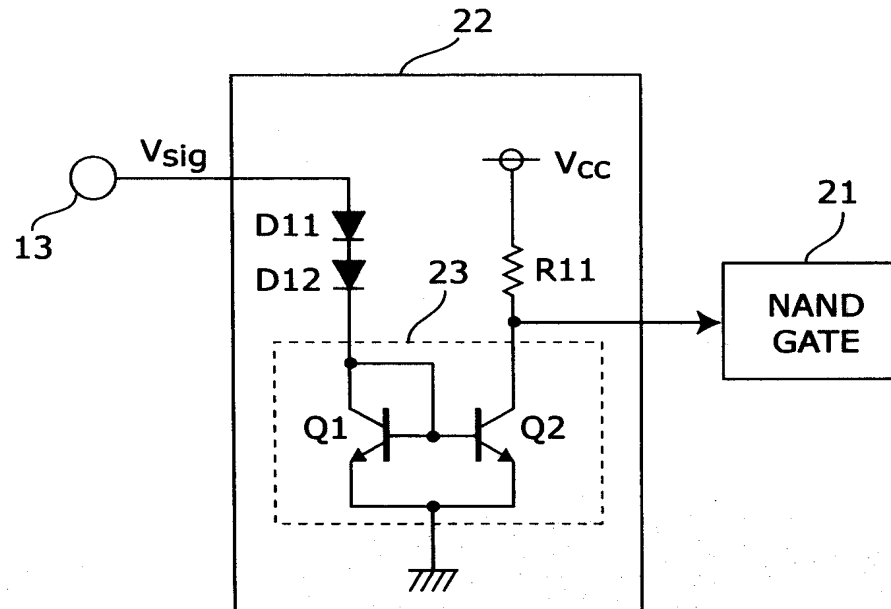


FIG. 4



The diagram illustrates a microcomputer system (72) with an output portion (60) and a control portion (67). The output portion (60) includes a drive circuit (66) and an output port (64). The control portion (67) includes an internal control circuit (74) and a reset input (63). The system is powered by V_{pp} (61) and V_{dd} (59). The output signal (S2) is connected to the output port (64) and the output signal (S2) is connected to the output port (64). The reset input signal (S3) is connected to the reset input (63) and the reset output signal (S4) is connected to the reset output (63). The control signal (S6) is connected to the internal control circuit (74) and the control signal (S6) is connected to the internal control circuit (74). The output signal (S2) is connected to the output port (64) and the output signal (S2) is connected to the output port (64). The reset output signal (S4) is connected to the reset output (63) and the reset output signal (S4) is connected to the reset output (63). The control signal (S6) is connected to the internal control circuit (74) and the control signal (S6) is connected to the internal control circuit (74).

FIG. 6

